



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: ERIC J. BERGMAN
APPLICATION NO.: 10/681,553
FILED: OCTOBER 7, 2003
FOR: **METHODS FOR CLEANING SEMICONDUCTOR
SURFACES**

EXAMINER:
ART UNIT: 1746
CONF. NO: 7774

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

1. Timing of Submission

This supplemental IDS is believed to be timely in that it is being submitted under 37 CFR § 1.97(b), that is (1) within three months of the filing date of the application, which is not a continued prosecution application filed under § 1.53(d); or (2) within three months of entry of the national stage as set forth in 37 CFR § 1.491; or (3) before the mailing of a first Office action on the merits; or (4) before the mailing of a first Office action after filing a request for continued examination under § 1.114. Thus, no fee is required. The references listed on the enclosed Form PTO-1449 (modified) may be material to the examination of this application; the Examiner is requested to make them of record in the application.

2. Cited References

Copies of all cited references are enclosed.

Certificate of Mailing

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

June 16, 2004
Date of Deposit

Debbie Gilbert
Debbie Gilbert

3. Effect of Information Disclosure Statement (37 C.F.R. § 1.97(h))

This Information Disclosure Statement is not to be construed as a representation that: (i) a search has been made; (ii) additional information material to the examination of this application does not exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the cited information is, or is considered to be, material to patentability. In addition, applicant does not admit that any enclosed item of information constitutes prior art to the subject invention and specifically reserves the right to demonstrate that any such reference is not prior art.

4. Fee Payment

No fees are believed due because this Information Disclosure Statement is being filed before the mailing of a first Office Action.

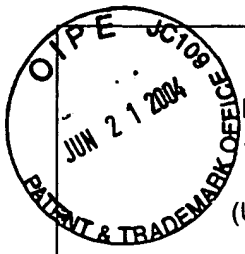
Dated: June 15, 2004

Customer No. 34055
Perkins Coie LLP
Patent - LA
P.O. Box 1208
Seattle, WA 98111-1208
Phone: (310) 788-9900
Fax: (310) 788-3399

Respectfully submitted,

PERKINS COIE LLP

By: Kenneth H. Ohriner
Kenneth H. Ohriner
Reg. No. 31,646



INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

Form PTO-1449 (Modified)
(Use several sheets if necessary)

COMPLETE IF KNOWN

Application Number	10/681,553
Confirmation Number	7774
Filing Date	October 7, 2003
First Named Inventor	Eric J. BERGMAN
Group Art Unit	1746
Examiner Name	
Attorney Docket No.	54008.8012.US04 (P96-0015US5)

Sheet	1	of	1
-------	---	----	---

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.	U.S. Patent or Application		Name of Patentee or Inventor of Cited Document	Date of Publication or Filing Date of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		NUMBER	Kind Code (if known)			
	EZ	6,551,409		DeGendt	04/22/03	
	FA	2002/0011257		DeGendt	01/31/02	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.	Foreign Patent or Application			Name of Patentee or Applicant of Cited Document	Date of Publication or Filing Date of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T
		Office	NUMBER	Kind Code (if known)				

OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume issue number(s), publisher, city and/or country where published.	T
	FB	Golland, D.E., <i>et al.</i> , "The Clean Module: Advanced Technology for Processing Silicon Wafers." <i>Semiconductor Int'l.</i> , pp. 154-157 (Sep. 1987).	

EXAMINER	DATE CONSIDERED
*EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance <u>and</u> not considered. Include copy of this form with next communication to application(s).	

[54008.8012.US04/LA041610.077]